

The PhD Forum of VLSI-SoC 2019

October 6-9, 2019 Cuzco, Peru



VLSI-SoC 2019's Ph.D. Forum is a poster session aimed at the exchange of ideas and experiences of Ph.D. students from different parts of the world. Elected Ph.D. students have an opportunity to discuss their thesis and research work with specialists within the system and design automation communities. This offers a good opportunity for students to receive valuable feedback and gain exposure in the job market. Furthermore, this forum also provides a great chance for industry officials to meet junior researchers, giving an avenue for incorporating the latest research developments into their companies.



Eligibility: The author must have completed at least one year of a Ph.D. program.

Presentation: Posters will be introduced in the Ph.D. Forum Session (two-minute time slot, one slide) and next presented in a full one-hour Poster Session.

Publication: Accepted posters will be given two pages in the soft proceedings.

Best PhD forum Poster Award: The PhD forum selection committee will review all presented posters and recommend one poster for the Best PhD Forum Poster Award. We will have awards for second and third places as well.

Grants for PhD students: VLSI-SoC 2019 finances a limited number of travel grants for Ph.D. students, through the Technical Committee TC-10 (Computer Systems Technology) and WG 10.5 of the International Federation for Information Processing - IFIP. In principle, grants are restricted to support students who cannot be fully supported by their institutions. This rule gives preference to students being enrolled in Africa, Latin America, Eastern Europe and Asia (except Israel, Japan, Singapore, South Korea, Taiwan and China).

How to Apply: Submit a two-page extended abstract of your research work in PDF format.

Send submissions via EASYCHAIR platform of VLSI-SoC2019 until May 31. Paper format should be compliant with the guidelines for regular papers.

Topics of interest include but are not limited to:

- Analog, Digital, and Mixed-Signal IC Design
- 3-D Integration and Physical Design
- SoC Design for Variability, Reliability, Fault Tolerance, and Test
- New Devices, MEMS, and Microsystems
- Digital Signal Processing and Image Processing SoC Design
- Prototyping, Validation, and Verification
- Modeling and Simulation
- Embedded Systems and Processors
- Hardware/Software Codesign
- Processor Architectures and Multicore SoCs
- Logic and High-Level Synthesis
- Low Power and Thermal-aware Design
- Reconfigurable SoC Systems for Energy and Reliability
- Dependable SoCs
- SoC in Dark Silicon Era

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