

VLSI-SoC 2019's Student Forum



October 6-9, 2019 Cuzco, Peru



VLSI-SoC 2019's Student Forum is a poster session aimed at the exchange of ideas and experiences of students from different parts of the world. Elected students have an opportunity to discuss their research work with specialists within the system and design automation communities. This offers a good opportunity for students to receive valuable feedback and gain exposure in the job market. Furthermore, this forum also provides a great chance for industry officials to meet junior researchers, giving an avenue for incorporating the latest research developments into their companies



Eligibility: Undergraduate, master and early PhD students are invited to submit your work.

Presentation: Posters will be presented in a full one-hour Poster Session.

Publication: Accepted posters will be given two pages in the soft proceedings.

Best Student Forum Poster Award: The Student forum selection committee will review all presented posters and recommend one poster for the Best Student Forum Poster Award. We will have awards for second and third places as well.

Grants for Students: VLSI-SoC 2019 finances a limited number of travel grants for students. In principle, grants are restricted to support underrepresentative groups (women, latin, and others) of students who cannot be fully supported by their institutions.

How to Apply: Submit a two-page extended abstract of your research work in PDF format. Send submissions via EASYCHAIR platform of VLSI-SoC 2019 until July 21. Paper format should be compliant with the guidelines for regular papers.

Topics of interest include but are not limited to:

- Analog, Digital, and Mixed-Signal IC Design
- 3-D Integration and Physical Design
- SoC Design for Variability, Reliability, Fault Tolerance, and Test
- New Devices, MEMS, and Microsystems
- Digital Signal Processing and Image Processing SoC Design
- Prototyping, Validation, and Verification
- Modeling and Simulation
- Embedded Systems and Processors
- Hardware/Software Codesign
- Processor Architectures and Multicore SoCs
- Logic and High-Level Synthesis
- Low Power and Thermal-aware Design
- Reconfigurable SoC Systems for Energy and Reliability
- Dependable SoCs
- SoC in Dark Silicon Era

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